

GATES



F1 Gate



G1



G2



G3



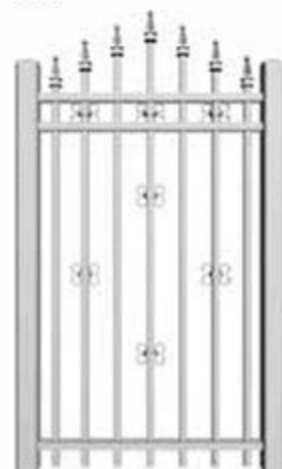
F2 Gate



G4



G5



G6